

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Name of Patentee:

John D. Heightley and Steve S. Eaton

Patent No.: 7,071,745

Issued: Jul. 4, 2006

Title: VOLTAGE-CONTROLLED ANALOG DELAY LOCKED LOOP

ATTENTION: Certificate of Corrections Branch

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

**REQUEST FOR CERTIFICATE OF CORRECTION OF PATENT FOR
PTO Mistake (37 C.F.R. 1.322(a))**

DEAR SIR:

An error appears in this patent. The error is a formatting mistake by the PTO. The error occurred in good faith. Correction thereof does not involve such changes in the patent as would constitute new matter or would require reexamination.

Attached hereto in duplicate is form PTO-1050, with at least one copy being suitable for printing.

Please send the certificate to:

William J. Kubida
Hogan & Hartson LLP
One Tabor Center
1200 17th Street, Suite 1500
Denver, CO 80202

Although no fee is believed due, any fee deficiency associated with this transmittal may be charged to Deposit Account 50-1123.

Respectfully submitted,

Date: 8/14/06

BY: Pete Meza

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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

Page 1 of 1

PATENT NO.: 7,071,745

APPLICATION NO.: 10/776,366

ISSUE DATE: Jul. 4, 2006

INVENTOR(S): John D. Heightley and Steve S. Eaton

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 10, line 9, "That" should be --that--

Mailing Address of Sender:

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Send to: Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450